

# **Paradigm Shifts in HPC**

Frank Baetke Cetraro HPC Workshop – July 7<sup>th</sup>, 2014





#### Think different.

Why use air, a commonly used "insulator", as the default heat removal mechanism?

# $h_{water} = 50-100 \text{ x } h_{air}$

 $h = \frac{Q}{A * \Delta T}$ 

h: heat transfer coefficient Q: heat input (W) A: heat transfer surface area (m<sup>2</sup>)  $\Delta$ T: Delta-T (K)





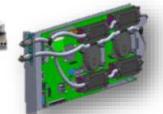


# Liquid Cooling today

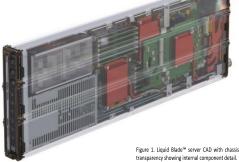
Components, cold-plates, immersion...









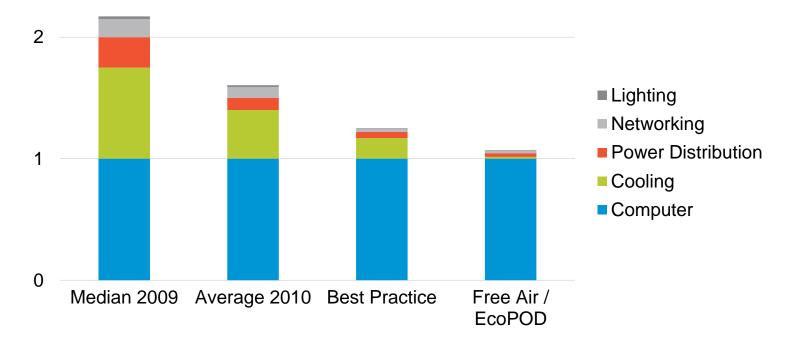






#### PUE

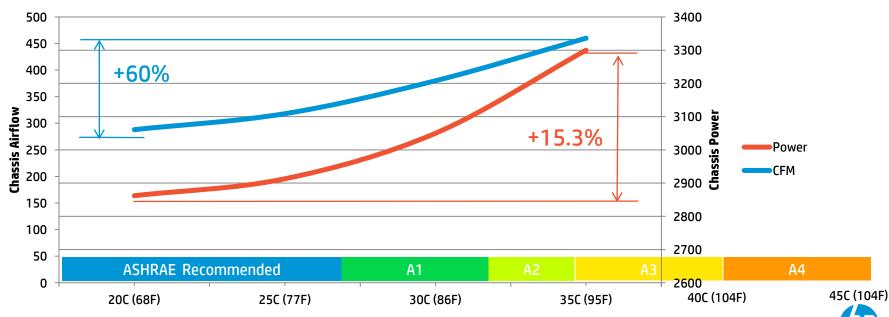
#### The "holy grail" of corporate IT?





### PUE: "the untold story"

When you CAN'T afford Free Air Cooling

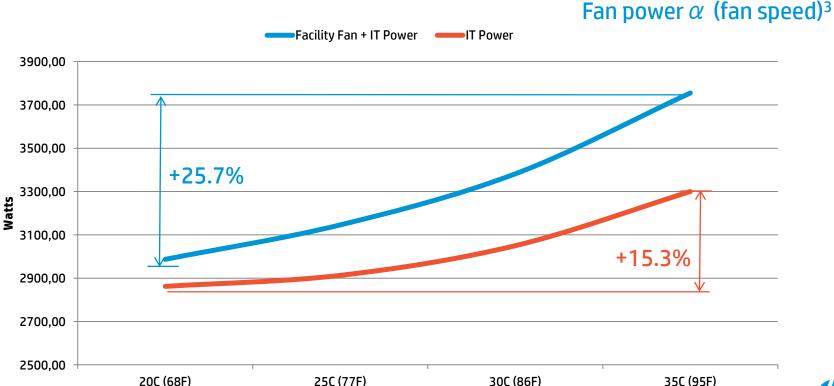


#### Server Environmentals vs Air Inlet Temperature

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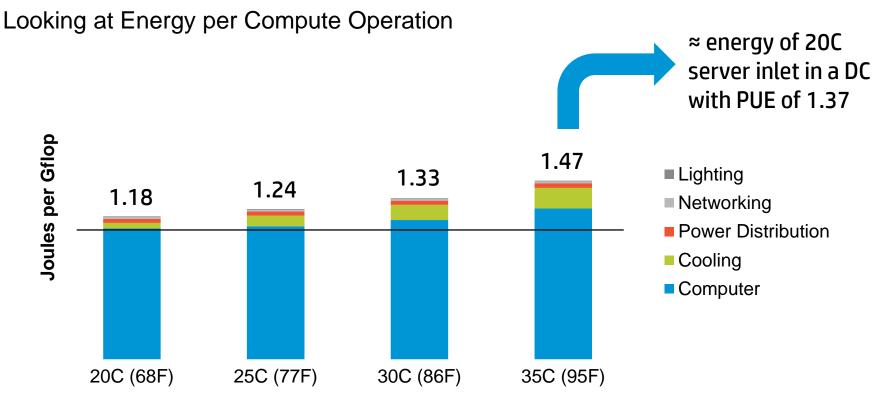
8x SL230 w/ E5-2670, 16x8GB, 4xSFF, Linpack

#### Free Air Cooling's "dirty little secret"





### What a ratio like PUE does not show





# Apollo Rack: Liquid Cooling made Comfortable

#### "Datacenter in a rack"

- Cooling
  - Liquid: CPUs, GPUs, DIMMs
  - Air to Liquid heat-exchanger: remaining components

#### • Power

- Up to 80kW (4x 30A 3ph 380-480VAC)
- Cooling capacity: up to 100kW

#### Supporting infrastructure

- Integrated Fabrics: InfiniBand, Ethernet, Management
- Pooled power, Battery backup unit...
- Taking IPMI to new levels





# The New HP Apollo 8000 System

Advancing the science of supercomputing



#### **Scientific Computing**

- Research computing
- Climate modeling
- Protein analysis

#### Manufacturing

- Product modeling
- Simulations
- Material analysis

#### Leading teraflops per rack for accelerated results

- Up to 150 teraflops/rack with compute trays
- > 250 teraflops/rack with accelerator trays

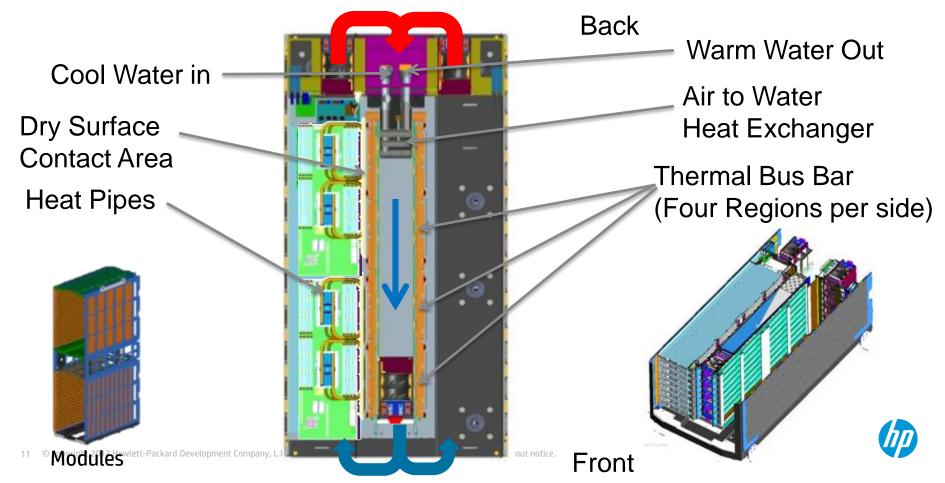
#### Efficient liquid cooling without the risk

- **Dry-disconnect** servers, intelligent Cooling Distribution Unit (iCDU) monitoring and isolation
- **Management** to enable facility monitoring, environmental controls and power management

#### Redefining data center energy recycling

- Save up to **3,800 tons** of CO2/year (790 cars)
- Recycle water to heat facility

# Apollo Rack - Hybrid Cooling Concept



# **Cooling Technology**

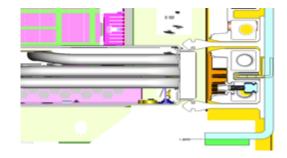
"dry-disconnect"









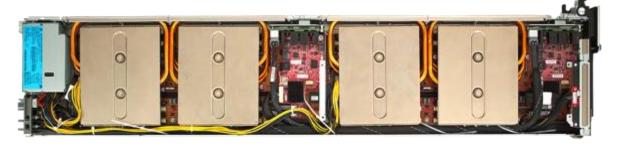






#### Trays

#### 2x 2 CPUs, 2CPUs + 2 Phis





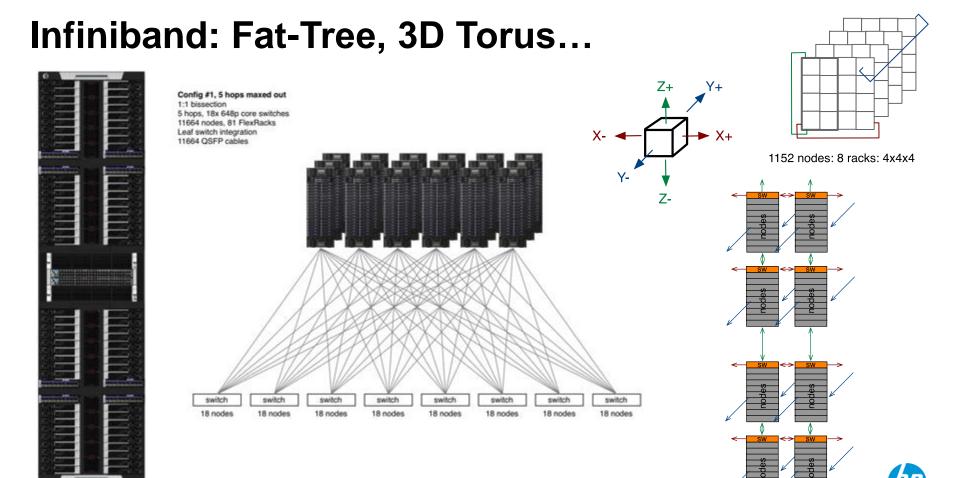










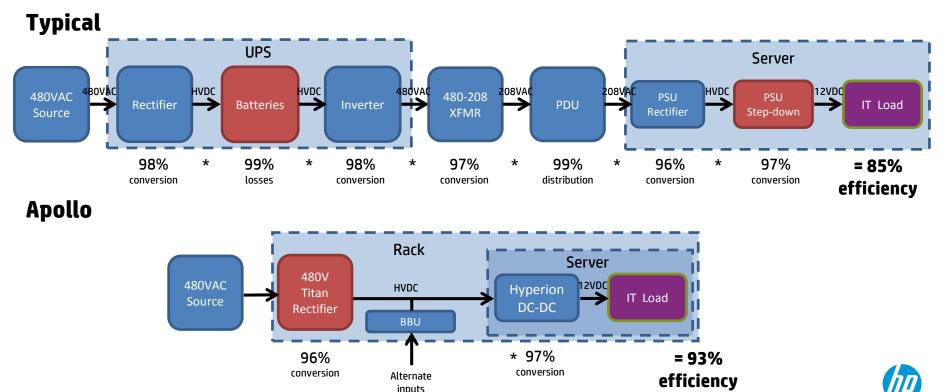


# **Power and Monitoring**



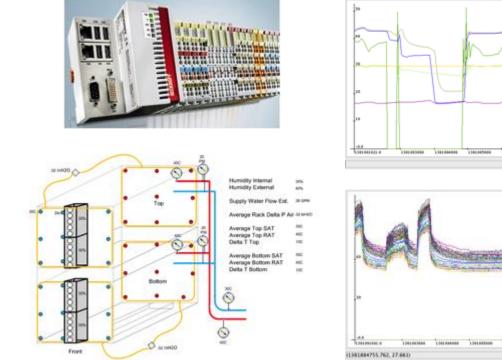
# **Power Distribution Efficiency**

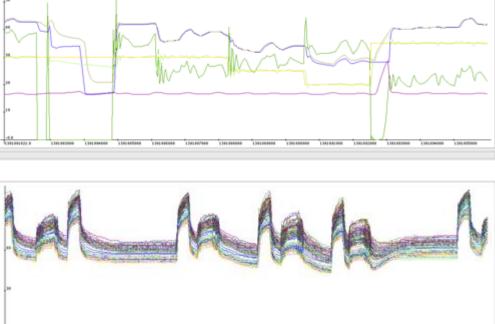
High-Voltage AC to the Rack: Limiting conversion steps



### **Sensors and monitoring**

You can't optimize what you can't measure





141.44444

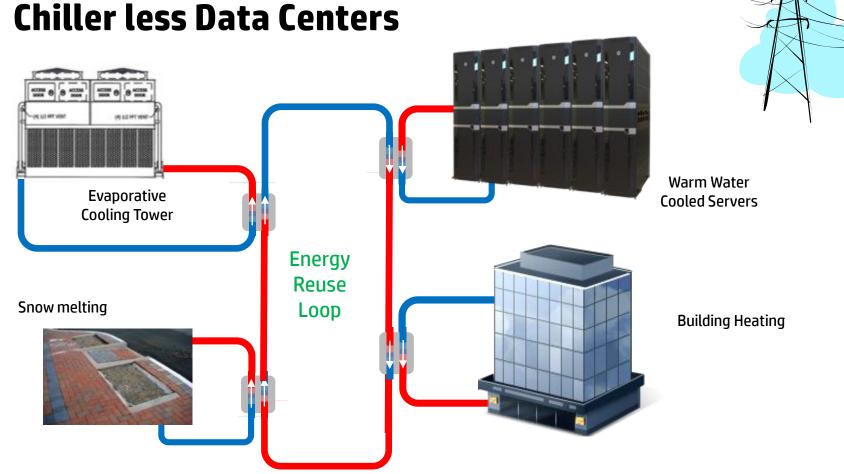
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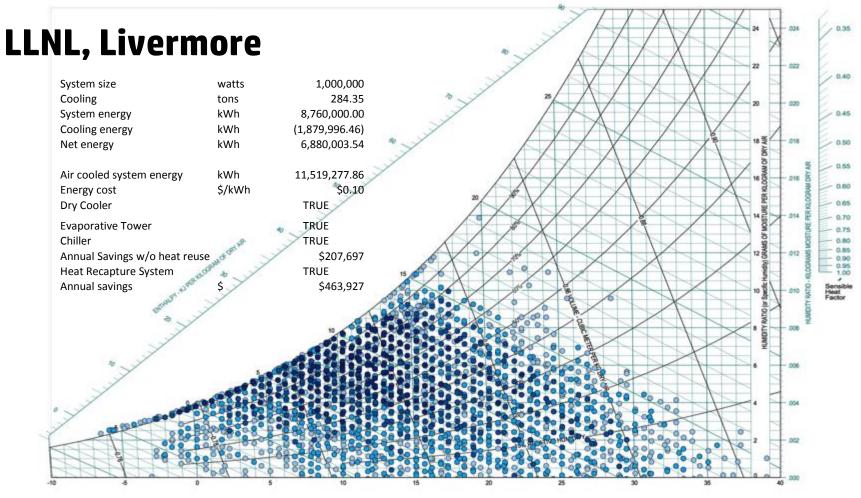
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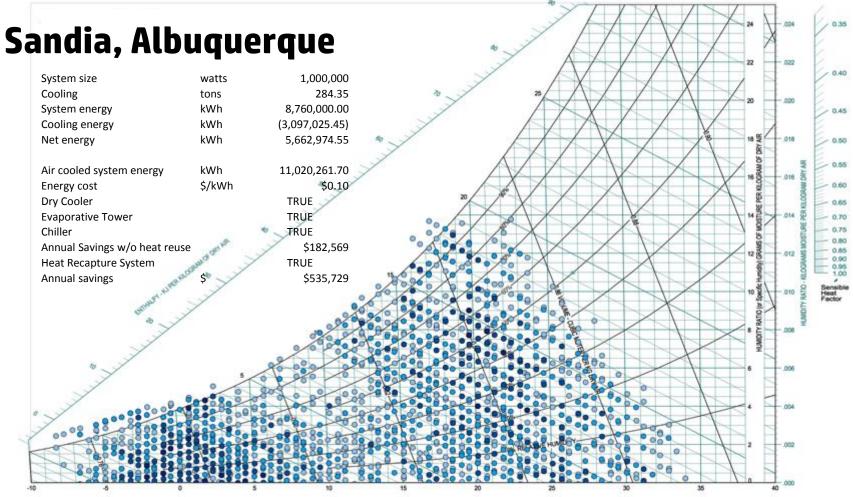




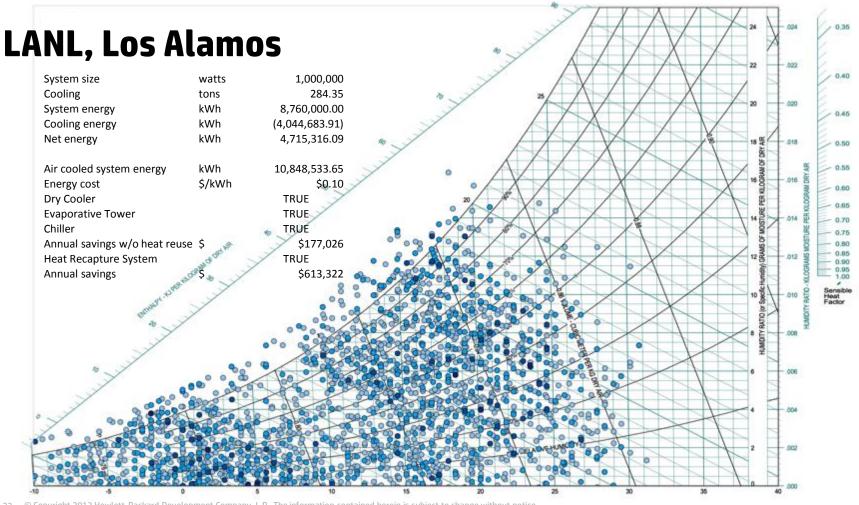












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### **University of Tromsø in Norway**

Forget cooling! Use the server room to heat the campus

# International research hub focuses on global environmental issues, up close

- Increasing research demands, # of advanced calculations
- Energy consumption/sq. meter went up dramatically, 2 megawatts with plans for more
- Building new 400 sq. meter data center
- Expect to reduce 80% of energy costs for computer operation, saving 1.5M krone in operating budget/year



"... the idea is to reduce electricity costs by sharing them with the rest of the university or other stakeholders heating." -Svenn A. Hanssen , Head of IT department at the University of Tromsø

# World's largest supercomputer dedicated to advancing renewable energy research



- **\$1 million in annual energy savings** and cost avoidance through efficiency improvements
- Petascale (one million billion calculations/ second)
- **6-fold increase** in modeling and simulation capabilities
- Average PUE of **1.06 or better**
- **Source of heat** for ESIF's 185,000 square feet of office and lab spaces, as well as the walkways
- 1MW of data center power in under 1,000 sq. ft., **very energy-dense** configuration

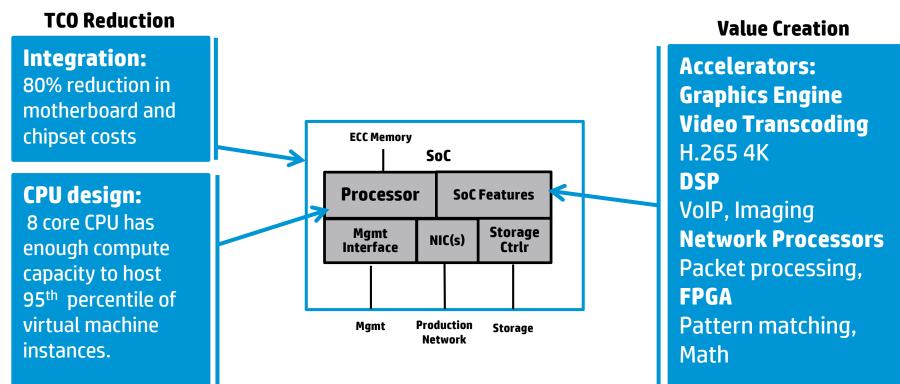




# Server SoC & Application Specific Compute

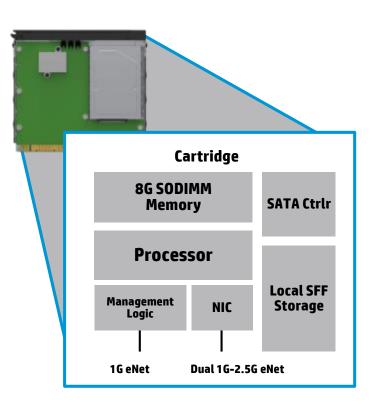


#### Server SoCs Bringing Disruptive Value





### Flexibility in cartridge design





- Complete server on every cartridge
- **45 servers** per chassis
- 450 servers per rack
- Example: dedicated server for hosting



### "Moonshot" up close (front view)

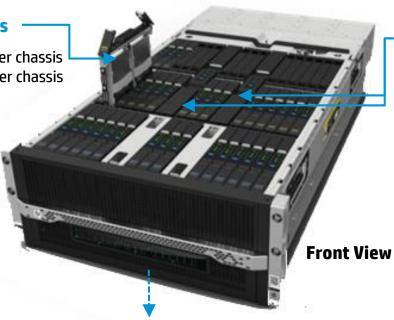
Delivering on the promise of extreme low energy computing

#### Top-loaded, hot plug cartridges

- Quad-Node cartridge =180 nodes per chassis
- Single-Node cartridge = 45 nodes per chassis



Compute, Storage, or Both, x86 and ARM



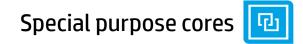
#### SL-APM and iPDU rack-level management

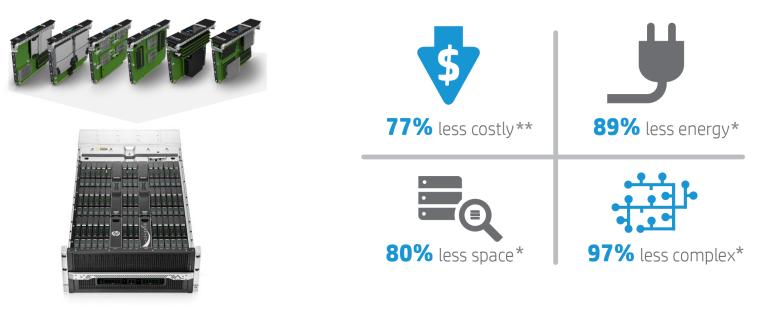
#### hp

#### Integrated A & B Switches

- 180x10G downlinks
- 6 x10G Stackable Uplinks







# **HP Moonshot is the first step**

- \* Based on HP internal analysis of HP Moonshot with ProLiant Moonshot Server Cartridges.
- \*\* Based on HP internal estimates of total cost to operate HP Moonshot with ProLiant Moonshot Server Cartridges as compared to traditional servers.





### BitCoin - An Application Specific Compute Example

Orders of Magnitude Improvement in Short Timeframe (YMMV)

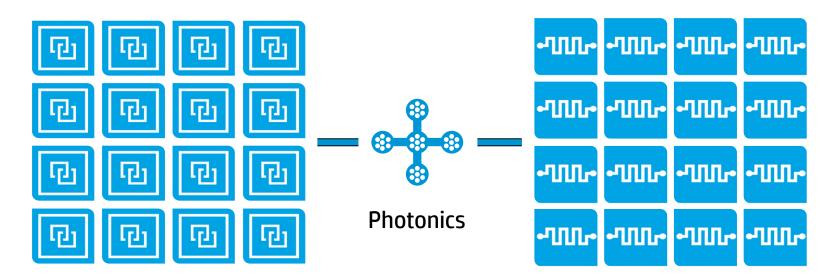
	X86 2011	GPU 2012	FPGA 2013	ASIC 2014
Million Hashes Per Second	7.5 1X	198 26X	800 105X	146,000 19,500X
Million Hashes per Joule	0.10 1X	1.3 13X	17.5 178X	913 9,300X

Source: Various Sampling of BitCoin Mining Hardware Performance https://en.bitcoin.it/wiki/Mining\_hardware\_comparison









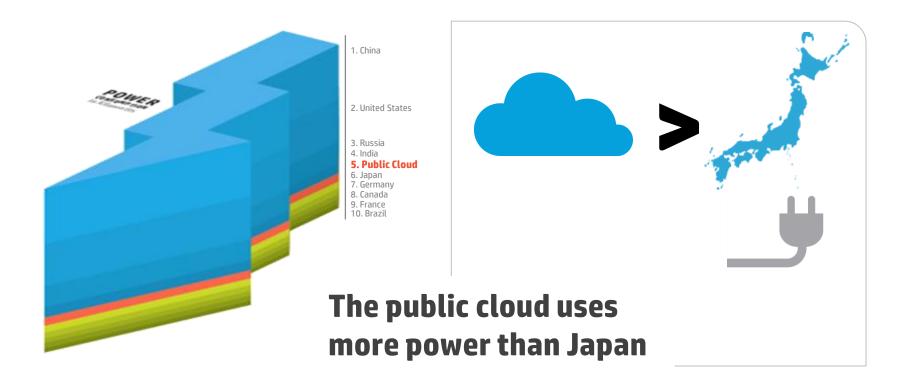
#### Special purpose cores

Massive memory pool

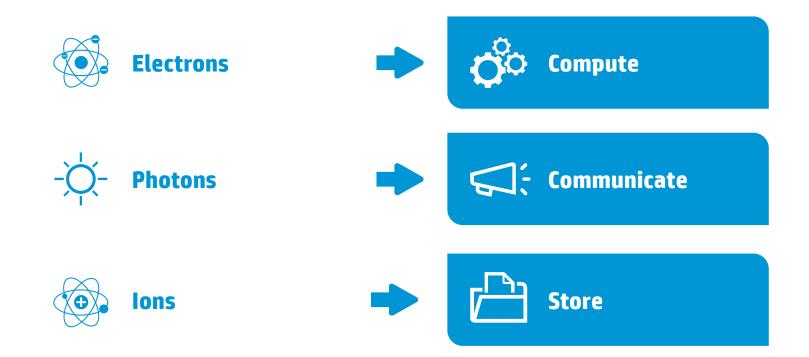
# **The Machine**



#### Exascale Challenge: 1,000X Compute @ 2X Energy





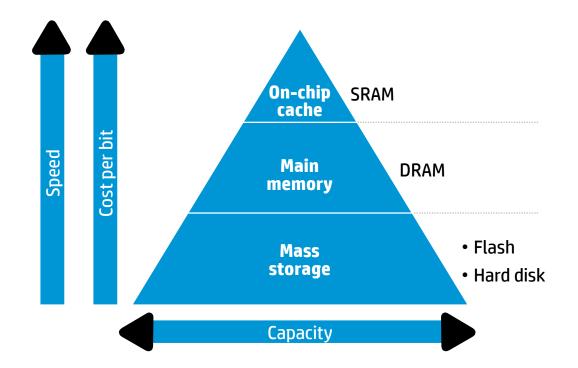




# **Universal Memory**



#### Massive memory pool

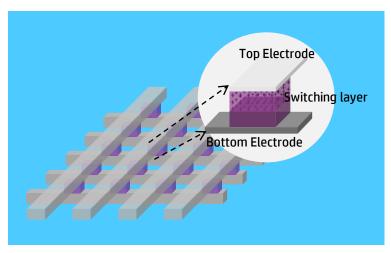


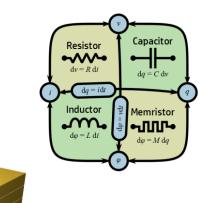
# Universal memory obsoletes this hierarchy



### **Universal Memory**

#### **HP Memristor**





Through-silicon-via technology for hard drive like densities

#### **Memristor Attributes**

- DRAM-like performance
- Extremely dense
- Stackable in-die

- Very low-power
- Thermals good for die stacking
- Silicon fab friendly



Photonics and Fabrics







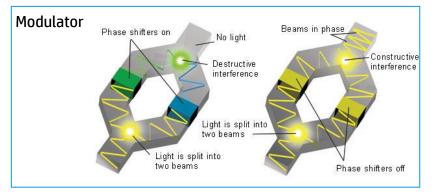
## **Photonics destroys distance**

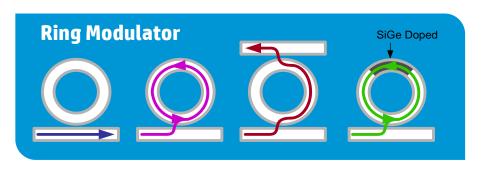


### Photonics



#### This is about power consumption and application efficiency





#### Industry investments in photonics

- Semiconductor lasers
- Light routing channels on silicon
- Light modulation by electrical signal
- Light path switching by electrical signal

#### Why photonics?

- High-bandwidth at extremely low power
- Distance matters little
- Compute subsystems can be redistributed for maximum space & thermal efficiency



### No one fabric to rule them all

Fabrics optimized for node count and workload

**Processing Nodes** 

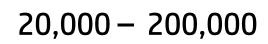
- SoC/Blade/Cartridge 2-20
- Zone/Chassis 20 200
- Rack

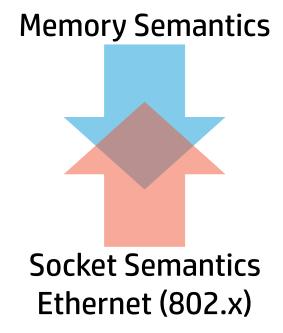
Row

Datacenter(s)

2,000 – 20,000

200 - 2,000

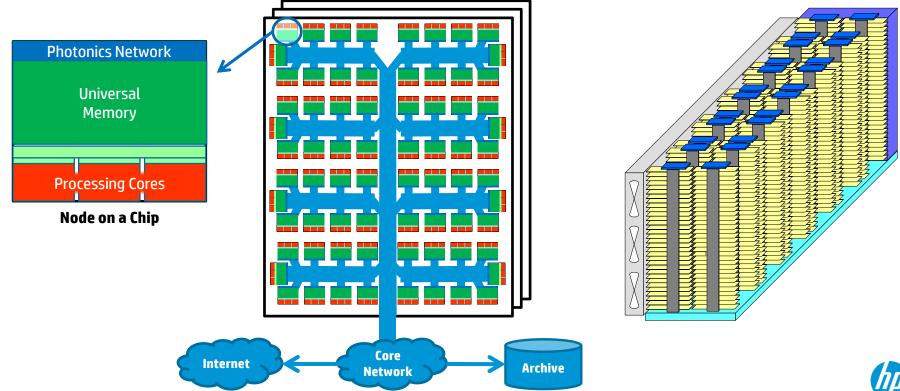






### **Technologies Working Together**

#### **Example 1: Massive Shared-Nothing Compute Farm**



### **Technologies Working Together**

#### Example 2: Data-Centric HPC System

