

## Data Centric Systems (DCS)

Architecture and Solutions for High Performance Computing, Big Data and High Performance Analytics

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# **Motivation**





### Data is Becoming the World's New Natural Resource





- 1 trillion connected objects and devices on the planet generating data by 2015
- 2 5 billion gigabytes of data generated every day
- Data is the new basis of competitive advantage



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### Systems of Insight are Important in Many Critical Economic Domains



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## Architecture



Massive data requirements drive a composable architecture for big data, complex analytics, modeling and simulation. The DCS architecture will appeal to segments experiencing an explosion of data and the associated computational demands



Principle 5: Leverage OpenPower to Accelerate Innovation

### **OpenPOWER Foundation**

**MISSION**: The OpenPOWER Consortium's mission is to create an open ecosystem, using the POWER Architecture to share expertise, investment and validated and compliant server-class IP to serve the evolving needs of customers

- Opening the architecture to give the industry the ability to innovate across the full Hardware and Software stack
  - Includes SOC design, Bus Specifications, Reference Designs, FW OS and Hypervisor Open Source
- Driving an expansion of enterprise class Hardware and Software stack for the data center
- Industry's first open system design for cloud data centers
- Building a vibrant and mutually beneficial ecosystem for POWER

## Example: POWER CPU NVIDIA GPU +

#### **Platinum Members**







# **POWER8** Chip Overview

- Up to 2.5x socket perf vs. POWER7+
- 649mm<sup>2</sup> die size, 4.2B transistors
- 12 high-performance cores
- Large Caches
  - L2: 512KB private SRAM per core
  - L3: 96MB shared eDRAM w/ 8MB "fast access" partition per core
  - L4: Up to 128MB, located on memory buffer chips
- 4 High Throughput I/O interfaces
  - Memory, On-Node SMP, Off-Node SMP, PCIe Gen3
- CAPI: open infrastructure for off-chip, memory-coherent accelerators

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(	On-Node SMP										
	Core	Core	Core	On Node SMP	Core	Core	Core				
Mem 0 -	L2	L2	L2		L2	L2	L2	o la co			
	L3	Quadra	nt	Acc	L3	Quadra	nt	Mem			
	MC	MC Fabric, Pervasiv					MC	4			
ω	L3	Quadra	nt	PCI	L3	3 Quadrant					
	L2	L2	L2		L2	L2	L2				
U	Core	Core	Core	Off Node SMP	Core	Core	Core				
PCIe3 Off-Node SMP											



### Commercially Viable System

- High Performance Analytics (HPA) and HPC markets
- Scale from sub-rack to 100+ rack systems
- Composed of cost effective components
- Upgradeable and modular solutions
- Holistic System Design
  - Storage, network, memory, and processor architecture and design
  - Market demands, customer input, and workflow analysis influence design
- Extensive Software Stack
  - Compiler, tool chain, and ecosystem support
  - O/S, virtualization, system management
  - Evolutionary approach to retain prior investments
- System Quality
  - Reliability, availability, serviceability



**New Data Centric Model** 





## Software

- Exascale systems must address science and analytics mainstream
  - -Broad range of users and skills
  - -Range of computation requirements: dense compute, viz, etc
- Applications will be workflow driven
  - -Workflows involve many different applications
  - -Complex mix of algorithms,
  - Strong Capability Requirements
  - -UQ, Sensitivity Analysis, Validation and Verification elements
  - Usability / Consumability
- Unique large scale attributes must be addressed
  - -Reliability
  - Energy efficiency
- Underlying data scales pose significant challenge which complicates systems requirements
  - Convergence of analytics, modeling, simulation, visualization, and data management





### Role of the Programming Model in Future Systems

IBM Research

- Recent programming models focus is on node level complexity
- Computation and control are the primary drivers
  - Data and communication are secondary considerations
  - Little progress on system wide programming models
    - MPI, SHMEM, PGAS ...
- Future, data centric systems will be workflow driven, with computation occurring at different levels of the memory and storage hierarchy
  - New and challenging problems for software and application developers
- Programming models must evolve to encompass all aspects of the data management and computation requiring a data-centric programming abstraction where:
  - Compute, data and communication are equal partners
  - High level abstraction will provide user means to reason about data and associated memory attributes
  - There will be co-existence with lower level programming models



### Some Strategic Directions – With Pragmatic Choices

- Refine the node-level model and extend to support system wide elements
- Separate programming model concerns from language concerns
- Revisit holistic language approach build on the 'endurance' of HPCS languages and evolve the concepts developed there:
   – Places, Asynchrony ..
- Investigate programming model concepts that are applicable across mainstream and HPC
- Focus on delivery through existing languages:
  - Libraries, runtimes, compilers and tools
- Strive for cross vendor support

Pursue open collaborative efforts – but in the context of integrated system development



**Data Centric Model** 





## Workflows







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- The ability to generate, access, manage and operate on ever larger amounts and varieties of data is changing the nature of traditional technical computing. Technical Computing is evolving, the market is changing
- The extraction from Big Data of meaningful insights, enabling real time and predictive decision making across a range of industrial, commercial, scientific and government domains, requires similar computation techniques that have traditionally been characteristic of Technical Computing
  - The era of Cognitive Supercomputers:
    - Convergence in many future workflow requirements
      - Big Data driven analytics, modeling, visualization, and simulation
- A time of significant disruption Data is emerging as the "critical" natural resource of this century
  - An optimized full system design and integration challenge
- Innovation in multiple areas, building off of an open ecosystem working with our OpenPOWER partners:
  - System architecture and design, modular building blocks
  - Hardware technology
  - Software enablement
  - Best in class resilience
- Development of DCS systems in close collaboration with technology providers and partners in multiple areas with focus on:
  - Workload-driven co-design
  - New hardware and software technologies
  - Programming models and tools
  - Open-source software