## Prospects for Monte Carlo Methods in Million Processor-core Era and Beyond

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# Outline

- Monte Carlo Methods as scalable algorithms
- Desirable characteristics of Pseudo-random Number Generator(PRNG)
- Parallelization of PRNGs
- Application Areas

# Trends in HPC Architecture

- Higher degree of parallelism (>Million cores)
- Diminishing memory size per core
- Imbalance between CPU power and Bandwidth of Interconnecting network
- More expensive to move data than F.P. computation
- Fault Resilience becoming very critical issue

### Advantages of Monte Carlo Methods

- Many traditional algorithms are to be re-examined because of bad scalability
- Monte Carlo Methods are "<u>embarrassingly parallel</u>" and highly scalable
- Monte Carlo Methods require <u>small memory size per core</u> because of geometry without discretization (no grid!)
- Monte Carlo Methods are fault-resilient (to some extent)



# John von Neumann wrote,



JOHN VON NEUMANN 1903-1957

"Any one who considers arithmetical methods of producing random digits is, of course, in a state of sin." (1951)

Middle-square method
Mapping function: f(x) = 4 x(1-x)

Various Techniques Used in Connection With Random Digits, Collected Works Vol. 5 pp.768-770 (1961)

### Important Features of PRNG Algorithms

- Long Period
- Good statistical quality (both serial and parallel)
- High speed generation
- Massively parallelizable in short time (ability to jump-ahead / parallel seeds)

### **Random Number Generation Algorithms**

(1) Linear Congruential Generator(LCG): X<sub>n</sub> = (a X<sub>n-1</sub> + c) mod M Multiplicative : c=0. → Period = 2<sup>j-2</sup> Mixed : c.ne. 0. → Period = 2<sup>j</sup> where M=2<sup>j</sup> (usually Machine Word Size)
The period is short (especially if j=32)

- Undesirable characteristics for certain applications
  - ( i.e. hyperplanes in higher dimensions)



### 64-bit LCGs Recommended by Forrest Brown(LANL)

LA-UR-05-4983 Fundamentals of Monte Carlo Particle Transport

#### L'Ecuyer's 63-bit LCGs

![](_page_7_Picture_3.jpeg)

- L'Ecuyer suggested 63-bit LCGs with good lattice structures. Math. Comp., 68, 249–260 (1999)
  - Good multipliers were chosen based on the spectral test.
  - Multiplicative LCGs
    - LCG(3512401965023503517, 0, 2<sup>63</sup>)
    - LCG(2444805353187672469, 0, 2<sup>63</sup>)
    - LCG(1987591058829310733, 0, 2<sup>63</sup>)
  - Mixed LCGs
    - LCG(9219741426499971445, 1, 2<sup>63</sup>)
    - LCG(2806196910506780709, 1, 2<sup>63</sup>)
    - LCG(3249286849523012805, 1, 2<sup>63</sup>)

C.E.Haynes: LCG(6364136223846793005,0,2^64) (Knuth, Vol.2, p.107-108)

(2) Binary M-sequence with Primitive Trinomial:

 $X_n = (X_{n-m} \oplus X_{n-k}) \mod 2 \rightarrow \text{Period} = 2^k - 1 \pmod{k}$ 

#### (Shift Register Sequence)

![](_page_8_Figure_3.jpeg)

### Mersenne Twister

- Evolved from a matrix formulation of M-sequence
   Xn +p := Xn+q + (Xn | Xn+1) A.
- Very very long period  $(2^19937 1)$
- Logical operations only
- May have issues of generating the seeds for MPP (My opinion)

![](_page_9_Figure_5.jpeg)

### (3) Generalized Fibonacci Method with Primitive Trinomial:

 $X_{n} = (X_{n-m} \text{ op. } X_{n-k}) \mod M \rightarrow \text{Period} = (2^{k}-1)2^{j-1} \sim 2^{k+j-1}$ where op. is {+, -, \*}.

![](_page_10_Figure_2.jpeg)

\* Ranlux is a modification to Generalized Fibonacci Method

### (4) Generalized Recursive Method With Large Prime Modulus

(Multiple Recursive Generator or MRG)

The Period is  $p^{k}-1 \sim 2^{j^{k}}$ , where  $p \sim 2^{j}$  is a Prime Number (e.g.,  $2^{31} - 1$ )

### Characteristics (and Advantages) of <u>MRG</u> with 8<sup>th</sup>-order Full Primitive Polynomials

From here on, consider  $p=2^{31}-1$  and k=8.  $f(x) = (x^8 - a_1x^7 - a_2x^6 - a_3x^5 - a_4x^4 - a_5x^3 - a_6x^2 - a_7x - a_8) \mod (p)$ 

(1) Better chance of finding a generator which possesses good Lattice Structure with non-constrained full coefficients  $d_{t} \ge 1/sqrt(1 + a_{1}^{2} + a_{2}^{2} + a_{3}^{2} + a_{4}^{2} + a_{5}^{2} + a_{6}^{2} + a_{7}^{2} + a_{8}^{2})$ 

(*Ref.* P.L'Ecuyer , INFORMS Journal on Computing, Vol.9, No.1, pp.57-60, 1997)

(2) Reasonably long period:  $(2^{31}-1)^8 - 1 \sim 4.5*10^{74}$ 

- (3) Ease of applying to vector/parallel processing, due to small dimension of the states
- (4) Many primitive polynomials to choose from:  $\phi (p^k - 1)/k/(p^k - 1) = 2.2\%$

(5) Simple and straight-forward implementation is possible

### A Sample Polynomial with Good Lattice Structure (LatMRG)

Ref: Prof. Pierre L'Ecuyer, Univ. Montreal

 $x_{n} = a_{1}x_{n-1} + a_{2}x_{n-2} + a_{3}x_{n-3} + a_{4}x_{n-4} + a_{5}x_{n-5} + a_{6}x_{n-6} + a_{7}x_{n-7} + a_{8}x_{n-8} \mod(p)$ 

where

a <sub>1</sub> = 1089656042	a <sub>5</sub> = 189748160
a <sub>2</sub> = 1906537547	a <sub>6</sub> = 1984088114
a <sub>3</sub> = 1764115693	a <sub>7</sub> = 626062218
a <sub>4</sub> = 1304127872	a <sub>8</sub> = 1927846343
$p = 2^{31} - 1 = 2147483$	3647
Figure of Merit: M32=.62	729

Out of 345597 tested polynomials with  $a_8$  as primitive roots, 31843 are primitive polynomials (9.2%).

# Other Reported Full Polynomials with Good Lattice Structure

3 <sup>rd</sup> Order:	3 <sup>rd</sup> Order: $x_n = a_1 x_{n-1} + a_2 x_{n-2} + a_3 x_{n-3}$ mod(p), where $p = 2^{31} - 1 = 2147483647$			
Gru	ube-Dieter	Dieter	L'Ecuyer	
a <sub>1</sub>	= 518175991	a <sub>1</sub> = 388425559	a <sub>1</sub> = 2021422057	
a <sub>2</sub>	= 510332243	a <sub>2</sub> = 227651891	a <sub>2</sub> = 1826992351	
a <sub>3</sub>	= 71324449	a <sub>3</sub> = 5412951	a <sub>3</sub> = 1977753457	
4 <sup>th</sup> Order: $x_n = a_1 x_{n-1} + a_2 x_{n-2} + a_3 x_{n-3} + a_4 x_{n-4} mod(p)$ , where $p=2^{31} - 1 = 2147483647$				
Ľ	Ecuyer			
a <sub>1</sub>	= 2001982722			
a <sub>2</sub>	= 1412284257			
a <sub>3</sub>	= 1155380217			
a <sub>4</sub>	= 1668339922			
Dof	http://cnunto mat ch	a ac at /recults /learl /com/or	Inada7 html	

Ref. http://crypto.mat.sbg.ac.at/results/karl/server/node7.html

### Performance Measurement of MRG8 on Machines with Different Architectures

As of	8/10/06
Rev. 06/14/11	

System	Architecture	Clock	Peak	Rate of
			Performance	Generation
		(GHz)	(Gflops)	(10 <sup>6</sup> dp rng/sec)
Fujitsu VPP5000	Vector (Proprietary)	.3	9.6	201.8
NEC SX-6	Vector (Proprietary)	.5	8.0	224.3
Fujitsu PrimePower HPC2500	Scalar (Sparc64V)	1.3	5.2	23.2
IBM p-series 690	Scalar (Power4)	1.3	5.2	11.4
SGI Altix3700	Scalar (Itanium 2)	1.3	5.2	39.7
AMD Opteron	Scalar	2.0	4.0	34.0
Intel Woodcrest Xeon	Scalar (1 core)	2.66	10.6	108.1
Fujitsu Primergy RX200S2	Scalar(Xeon EM64T)	3.6	7.2	79.9
Fujitsu PrimeQuest480	Scalar (Itanium 2)	1.5	6.0	80.3
Fujitsu PrimeQuest RXI300	Scalar (Itanium2)	1.6	6.4	83.5
RIKEN/Fujitsu "K"	Scalar (SPARC64VIIIfx) (1 core)	2.0	16.0	34.5 (2 Integer Ops./clock)

### Order of Recurrence vs Rate of Generation

![](_page_16_Figure_1.jpeg)

# Empirical Test of PRNG (TestU01)

- TestU01 is a set of utilities for testing RNG
   P. L'Ecuyer and R. Simard, University of Montreal
- Three pre-defined batteries of tests
  - SmallCrush
    - 15 statistical tests, uses 2<sup>7</sup> random numbers
  - Crush
    - 186 statistical tests, uses 2<sup>35</sup> random numbers
  - BigCrush
    - 234 statistical tests, uses 2<sup>38</sup> random numbers

![](_page_18_Picture_0.jpeg)

## **BigCrush Test of PRNGs**

#### SPRNG

- Pass: LFG, MLFG, CMRG
- Fail: LCG, LCG64, PMLCG

#### TRNG

- Pass: LCG64\_shift, MRG3, MRG4, MRG5, MRG3s, MRG5s, YARN2, YARN3, YARN4, YARN5, YARN3s, YARN5s
- Fail: LCG64, MRG2, MT19937, MT19937\_64z

#### Random123

Pass: MRG8

Pass: Threefry, Philox, AES NI, ARS

#### MRG8

### Parallelization (1) ~ Random Initialization ~

Same as "Birthday Paradox" (e.g., Feller)

- Period of Random Numbers : N
   Usage of Random Numbers in each process/thread : n
   Total number of chunks which should not overlap: N/n = m
   Total Number of Cores : p
- Probability of no collision = 1 · (1-1/m) · (1-2/m) · · · · (1-(p-1)/m)
   ~= 1 -1/m-2/m- · · · · -(p-1)/m = 1 (p-1)p/(2 m)

Probability that at least one collision occurs  $\sim = (p-1)p/(2m)$ 

Example: N=10^74, n=10^15, m=10^59, p=10^6  $\rightarrow$  **<u>~5.10^(-48)</u>** 

But, Donald Knuth's advice is:

"Random number generators should not be chosen at random"

Parallelization (2) ~ Jump-Ahead (First Order Recurrence) ~

![](_page_20_Figure_1.jpeg)

# Parallelization (2) Jump-Ahead (the 8th Order Recurrence) ~

 $x_{n} = a_{1}x_{n-1} + a_{2}x_{n-2} + a_{3}x_{n-3} + a_{4}x_{n-4} + a_{5}x_{n-5} + a_{6}x_{n-6} + a_{7}x_{n-7} + a_{8}x_{n-8} mod(p)$ 

Define a Transfer Matrix A:

Then x' = A x Mod(p).

### Jumping Ahead the Sequence of MRG

![](_page_22_Figure_1.jpeg)

### Jump-Ahead for Arbitrary Distance

In order to compute  $x_n = A^n x_0 \mod(p)$  for an arbitrary *n*: (1) Compute and store  $A_j = A^{2^j} \mod(p)$  (j=0,1,2,3,4,.....). (2) Represent "n" in the binary form, e.g.,  $(b_{m-1},....,b_2,b_1,b_0)$ . (3) Multiply  $A_j \mod(p)$ 's together only when  $b_j=1$ (j=0,....,m-1), to obtain  $A^n$ .

Note: The same strategy works with the polynomial formulation with fewer arithmetic operations (Knuth).

### Fujitsu SPARC64 IXfx specifications

![](_page_24_Picture_1.jpeg)

Item	Specification
No. of cores	16
Level 2 cache	12 MB
Operating frequency	1.848 GHz
Process technology	40-nm CMOS
Die size	$21.9~\mathrm{mm} imes22.1~\mathrm{mm}$
No. of transistors	Approx. 1.87 billion
Peak performance	236 gigaflop/s
Memory bandwidth	85 GB/s (theoretical peak value)
Power consumption	110 W (process condition: TYP)

Implementation of MRG8 on Fujitsu FX10 (16 cores/node, clock:1.848 GHz)

- Parallelization for 16 threads with OpenMP
- Initialization Time = 0.208 + .696\*M ([]sec) where M is the number of 1's in the binary representation of the Jump distance N
- Generation Time of a PRN = 0.031 ([sec])

=> 32 Million PRN/sec/thread

• Work in Progress (MPI version across nodes)

### Random Number Testing on Quantum Diffusion Monte Carlo Application

![](_page_26_Figure_1.jpeg)

Figure 1. The ground state energies of He atom evaluated by several RNGs in VMC. Data is shown as the deviation from that by RANLUX-4.

Ref. K.Hongo, R.Maezono and K.Miura, J. Comp. Chem. **31**, pp.2186-2194 2010

### **Revisiting Monte Carlo Methods**

The Monte Carlo Methods (MCMs) were systematically studied in the early days of computing in various application areas.

- Elliptic Partial Differential equations
  - $\Delta u = 0$  (Laplace Equation
  - $\Delta u = f$  (Poisson's Equation)
  - △u Illu=0 (Linearized Poisson-Boltzmann Eq.) with u=g on the boundary (Dirichlet B.C.)
- System of Linear Equations
- Eigenvalue Problems

# Example: Laplace's Equation

#### Potential calculation in electromagnetics etc

$$\Delta \Psi = 0 \text{ in } \Gamma$$
$$\Psi = g \text{ on } \gamma$$

- 1. Finite Difference Method/finite Element Method
- 2. Boundary Element Method
- 3. Monte Carlo Methods
  - Walk on Spheres Method
  - Walk on Rectangles Method
  - Walk on Boundary Method

# Comparison of Three Methods

- FDM/FEM → All the values on the grid need to be calculated
- BEM → The boundary equation has to be re-calculated everytime the boundary values are changed
- MCM  $\rightarrow$ 
  - There is no Grid points (Direct calculation)
  - even if the boundary values are dynamically modified, calculation can be done immediately

### Example: Laplace's equation (cont.)

No need to perform random walks on grid points →Walk on Spheres (WOS) Method\*

![](_page_30_Figure_2.jpeg)

Mervin F. Muller

\*

The Annuls of Mathematical Statistics, Vol. 27, No. 3 (Sep., 1956), 569-589.

### Example: (x=0, y=0, ε =.0001)

No. of Walks	Solution	Final Radius	No. of RNs
10,000	.5098	1.3661 <b>10</b> <sup>-5</sup>	121,291
40,000	.50265	1.06 012 <b>10</b> <sup>-5</sup>	484,677
1,000,000	.499734	1.71094 <b>10</b> <sup>-6</sup>	12,083,215
Exact	.50000		

 $\epsilon$ = 10<sup>-4</sup>: takes ~12 Walks on the average (Theoretically proportional to log( $\epsilon$ )

# Conclusion

- With the recent trends in HPC architecture in the Million-core era, some of the traditional numerical algorithms need to be reconsidered due to their poor scalability.
- Monte Carlo Methods can be used to efficiently solve wider class of problems
- Random number support for massively parallel processing is essential.

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